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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
(use as many sheets as necessary)

Application Number: 10/637,844  
 Filing Date: August 8, 2003  
 First Named Inventor: Hui Jin  
 Group Art Unit: 2183  
 Examiner Name: Not yet assigned

Sheet 1 of 1 Attorney Docket No.: Flarion-78APP1 (103)

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines where relevant Passages or Figures appear
SMB	AA	3,542,756	Nov 24 1970	Gallager	
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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Office <sup>3</sup> Number <sup>4</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
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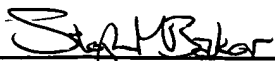
Examiner Signature	Stephen M. Baker	Date Considered	9/15/05
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1 Unique citation designation number. 2 See attached kinds of U.S. Patent Documents. 3 Enter Office that Issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the Indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16, if possible. 6 Applicant is to place a check mark here if English language translation is attached.

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		Group Art Unit: <b>2631</b>	
		Examiner Name: <b>Not yet assigned</b>	
Sheet	<b>2</b>	of	<b>2</b>
		Attorney Docket No.: <b>Flarion-78APP1 (103/1)</b>	

OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume, issue number(s), publisher, country, where published, source	T <sup>2</sup>
SMR	CA	Richardson et al. The capacity of low-density parity-check codes under message-passing Decoding, IEEE Transactions on Information Theory; pages: 599-618, February 2001, (same inventor) whole document.	
SMR	CB	Paranchych et al. Performance of a digital symbol synchronizer in cochannel interference and noise, IEEE Transactions on Communications, pages: 1945-1954; Nov. 2000, whole document.	
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SMR	CK	Mohammad M. Mansour, Naresh R. Shanbhag, Session 11: Low-power VLSI decoder architectures for LDPC codes, Proceedings of the 2002 international symposium on Low power electronics and design August 2002, Pages: 284-289.	
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